

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shigeru ATSUMI

Serial No. 09/028,276

Filed: February 24, 1998

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND FLASH
EEPROM



#11/B
9/30/99
V. Lassad
Group Art Unit: 2815

Examiner: Jesse A. Fenty

Atty Dkt No. 1701.73982

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SEP 30 1999
TC 2800 MAIL ROOM

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination on the merits, please amend the above-identified application as

IN THE CLAIMS:

Please amend the claims as follows:

1. (Amended) A semiconductor integrated circuit device comprising:

a semiconductor substrate on which a plurality of transistors including gate insulation films of different thicknesses are formed; and

B1
an input/output terminal formed on the semiconductor substrate, wherein a transistor physically connected directly to the input/output terminal is one of the transistors other than a transistor having the thinnest gate insulation film.

Please add new claims 21-42 as follows:

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--21. (New) A semiconductor integrated circuit device comprising: